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APPLICANTS

Justin K. Brask, Portland, OR;

Jack Kavalieros, Portland, OR;  
 Mark L. Doczy, Beaverton, OR; Uday Shah, Portland, OR;  
 Chris E. Barns, Portland, OR;  
 Matthew V. Metz, Hillsboro, OR;  
 Suman Datta, Beaverton, OR;  
 Annalisa Cappellani, Portland, OR;  
 Robert S. Chau, Beaverton, OR;

\*\* CONTINUING DATA \*\*\*\*\*  
*None*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
*None*

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35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	Examiner's Signature <i>BKS</i>	Initials		

ADDRESS

Michael A. Bernadicou  
 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP  
 Seventh Floor  
 12400 Wilshire Boulevard  
 Los Angeles, CA  
 90025

TITLE

Method for making a semiconductor device having a high-k gate dielectric layer and a metal gate electrode

☐ All Fees